functional block diagram
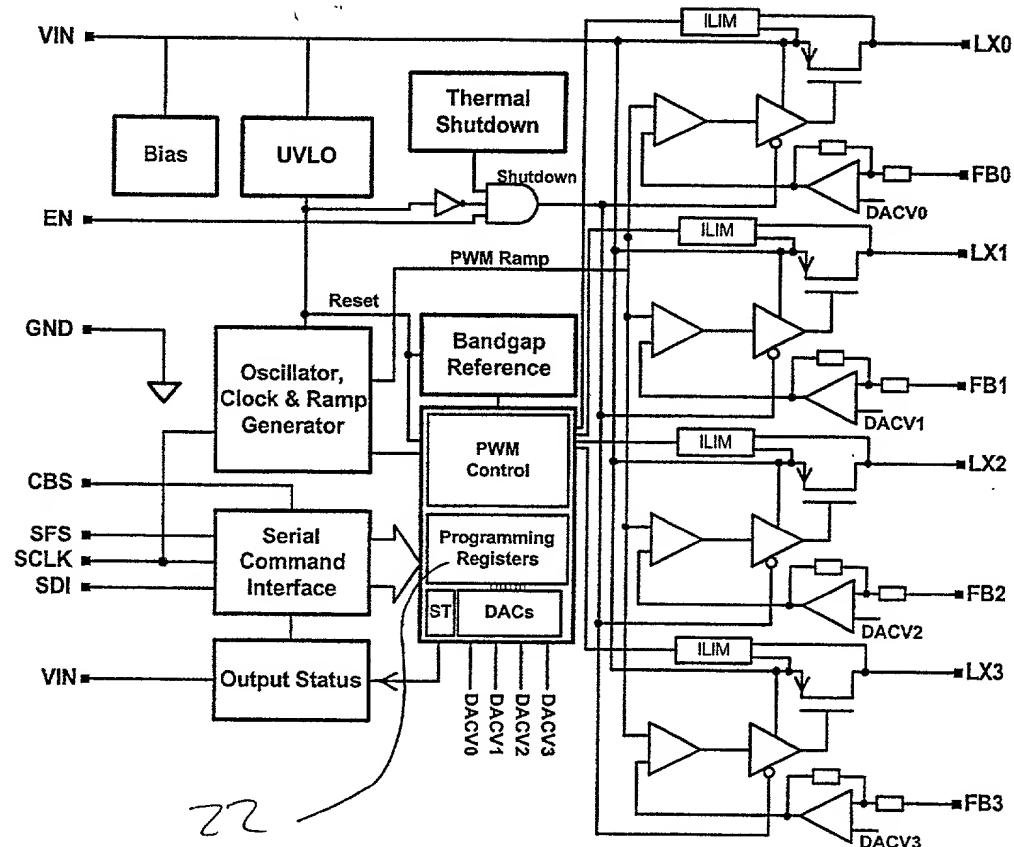

FIGURE 1

serial control interface timing diagram

FIGURE 3

eight channel application circuit schematic


FIGURE 2

SCLK

Initialization Word

Device 1
÷8 Counter State

Device 2
÷8 Counter State

Device 1
÷8 Output

Device 2
÷8 Output

IDENTIFY PART

16 BITS

Load 0
... 0 0 0 1 2 3 4 5 6 7 0 1 2 3 4 5

Load 4
... 4 4 5 6 7 0 1 2 3 4 5 6 7 0 1

180° out-of-phase
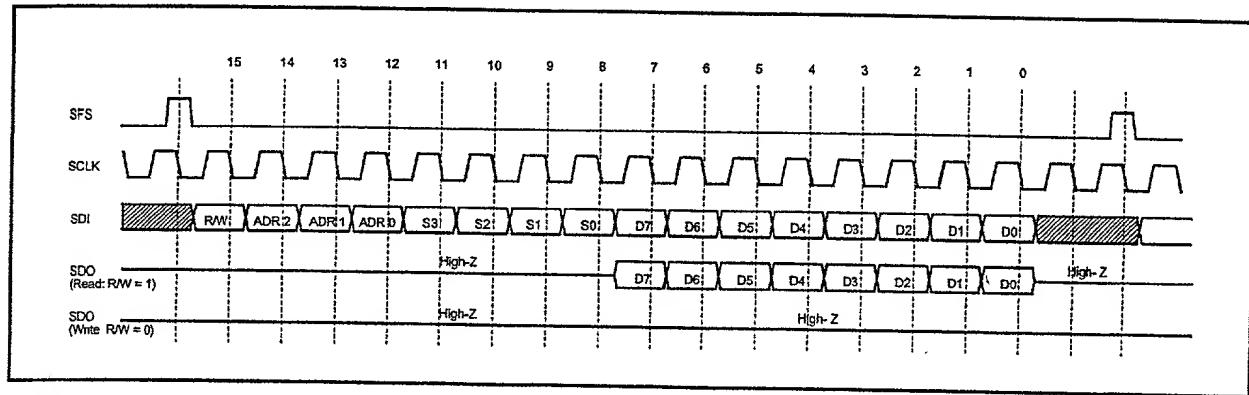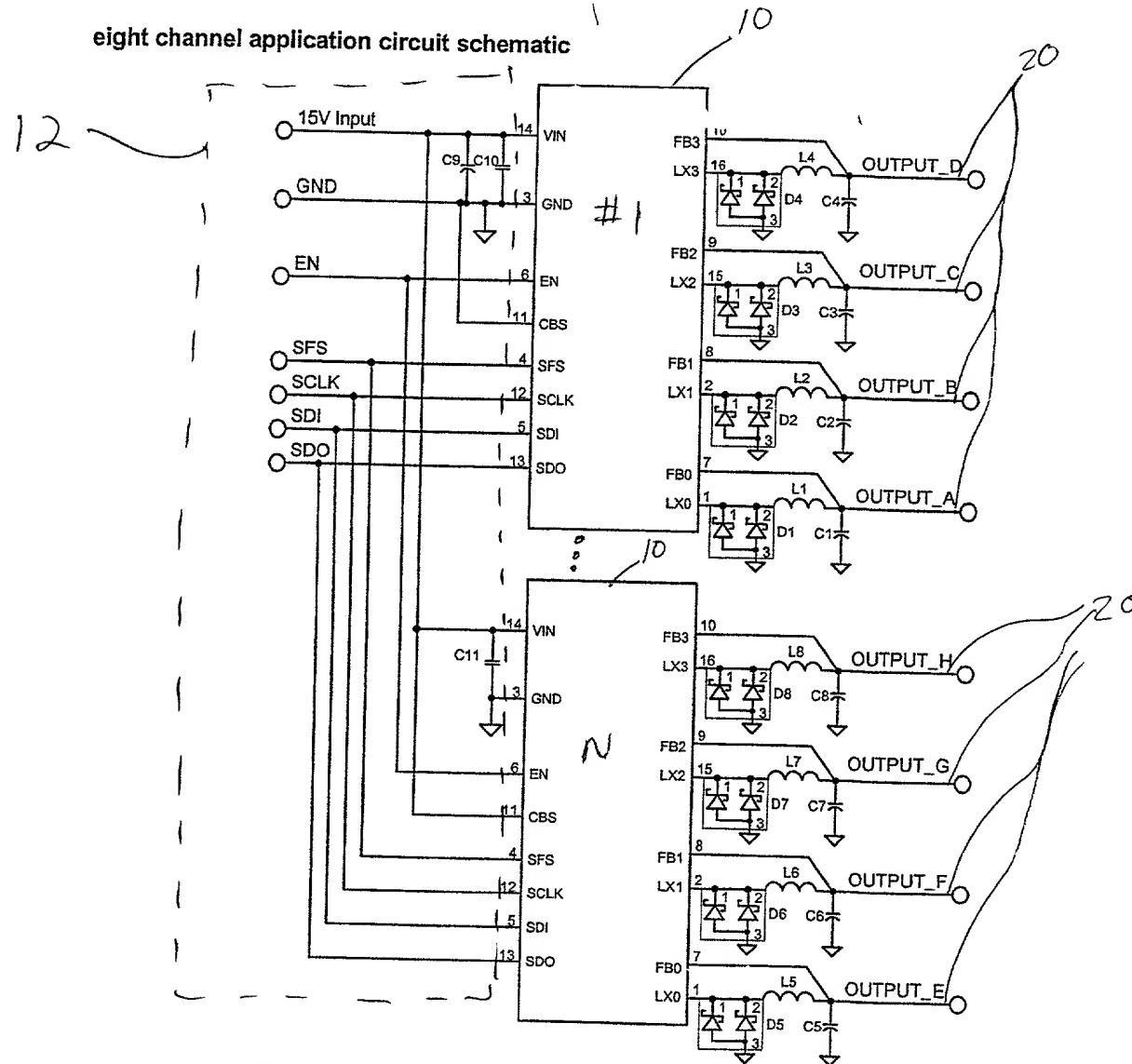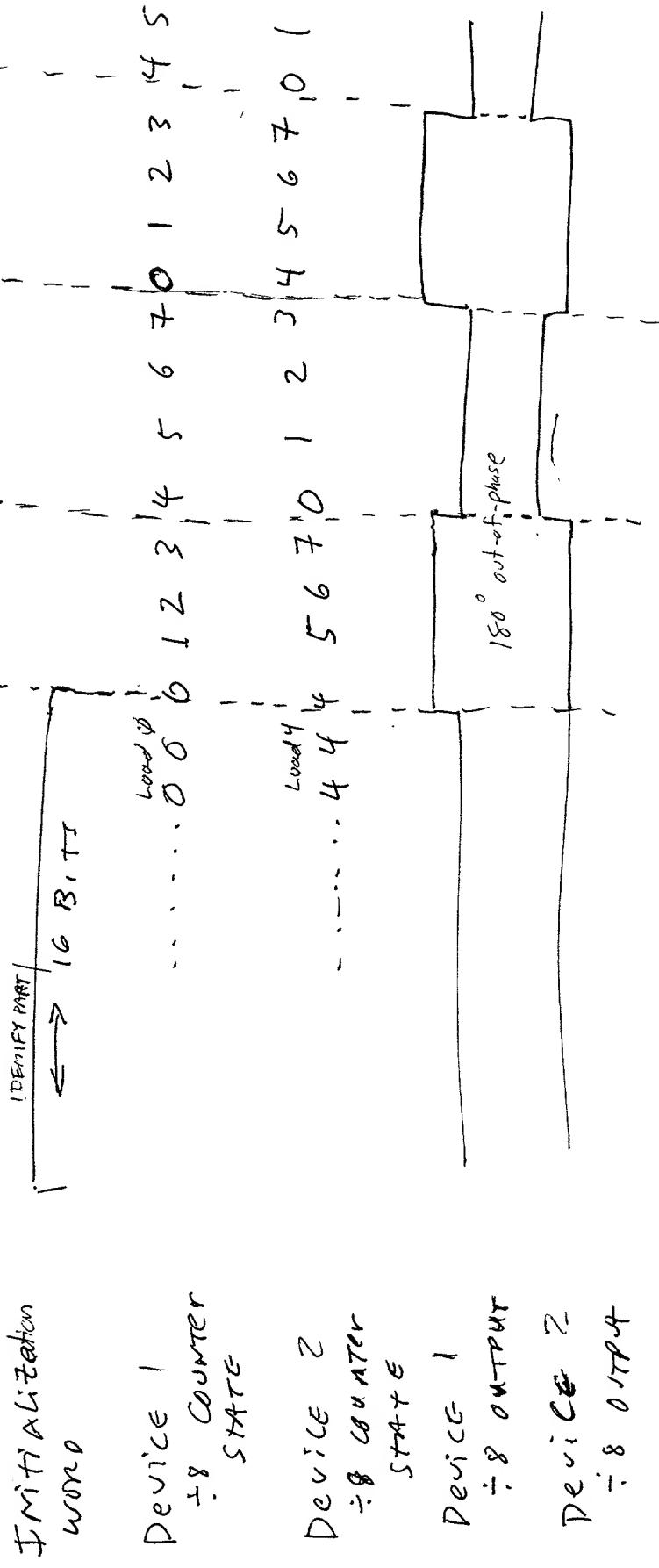
FIGURE 4

block diagram of eight channel AC5 line card with LineRanger option

FIGURE 5

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